



Alternative processor architecture support for CERN CentOS 7

Code	IT7544
Programme	FCT
Department	IT
Responsible	75479 - Mr. Timothy Giles Bell
Title	
Alternative processor architecture support for CERN CentOS 7	
Description	
<p>Processor architectures such as ARM and POWER are being exploited by the LHC and other CERN experiments aiming for lower power and higher performance. A production quality infrastructure is needed to support these activities.</p> <p>The candidate would work with the CentOS community and CERN Linux support team to enable alternative architectures (AARCH64, PPC64LE) in CERN workflow.</p> <p>Work with Red Hat and CentOS community</p> <p>Enable PXE/UEFI network booting for new architecture integration in AIMS2 installation system</p> <p>Port open source community and CERN tools to the new architectures</p> <p>Adapt the CERN Koji build system</p> <p>CERN CentOS 7 for new architecture integrated distribution</p> <p>Enhance the update processes for the new architecture</p>	
Skills	
<p>Information Technologies: Developing distributed computing systems (e.g. clusters, batch systems), System administration (e.g. with GNU/Linux, Microsoft Windows, Networks), Using software development tools (e.g. Git, Jira, Trac).</p> <p>Programming Languages: C, Python, Ruby, Shell Script</p> <p>Requirements: Good Linux OS knowledge, Scripting, system administration skills</p>	
Disciplines	
Information Technologies	

To edit this project go to https://hrapps.cern.ch/auth/f?p=131:4:::::P4_ID:7544