



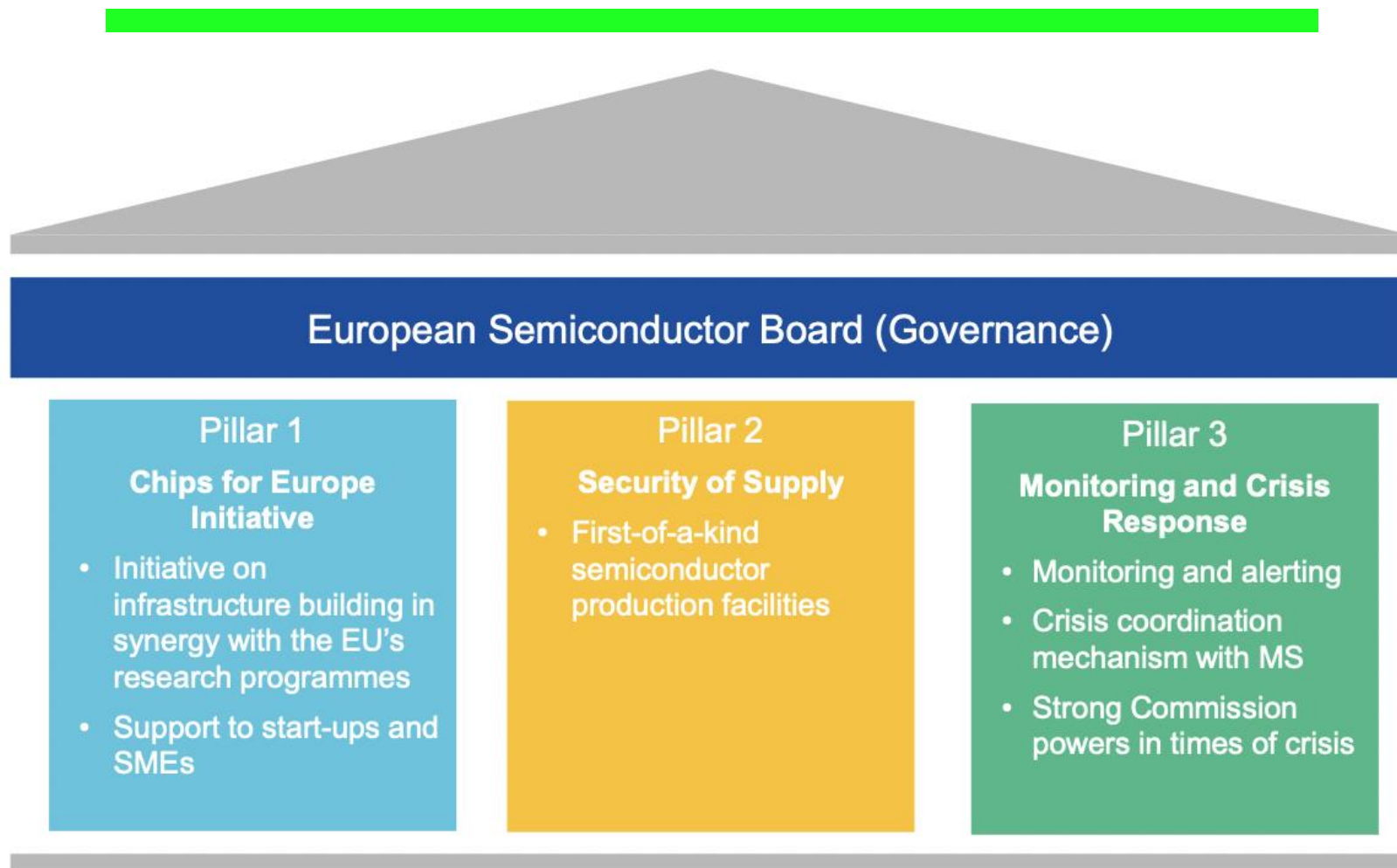
AGÊNCIA NACIONAL
DE INOVAÇÃO

CHIPS JU “Initiative” calls

04.03.2024



EU Chips Act



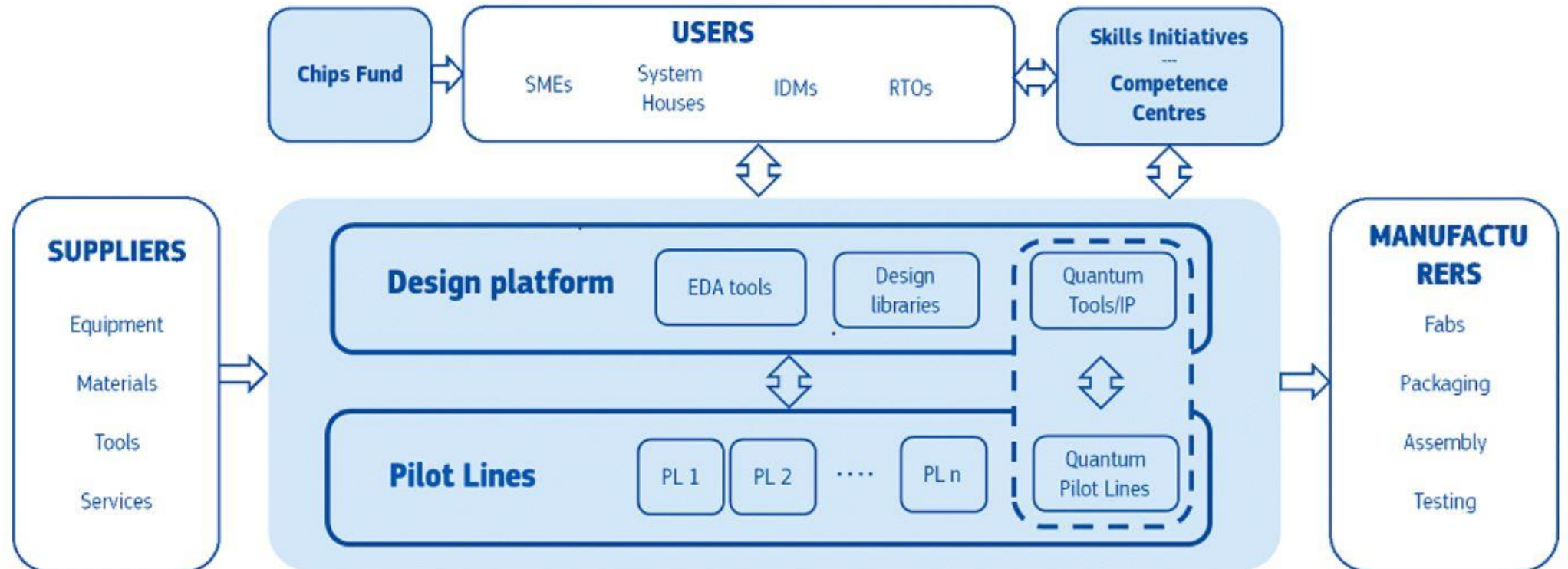


EU Chips Act Pillar 1

Develop and build infrastructures in synergy with R&D+I activities, supporting start-ups, SMEs and large companies.

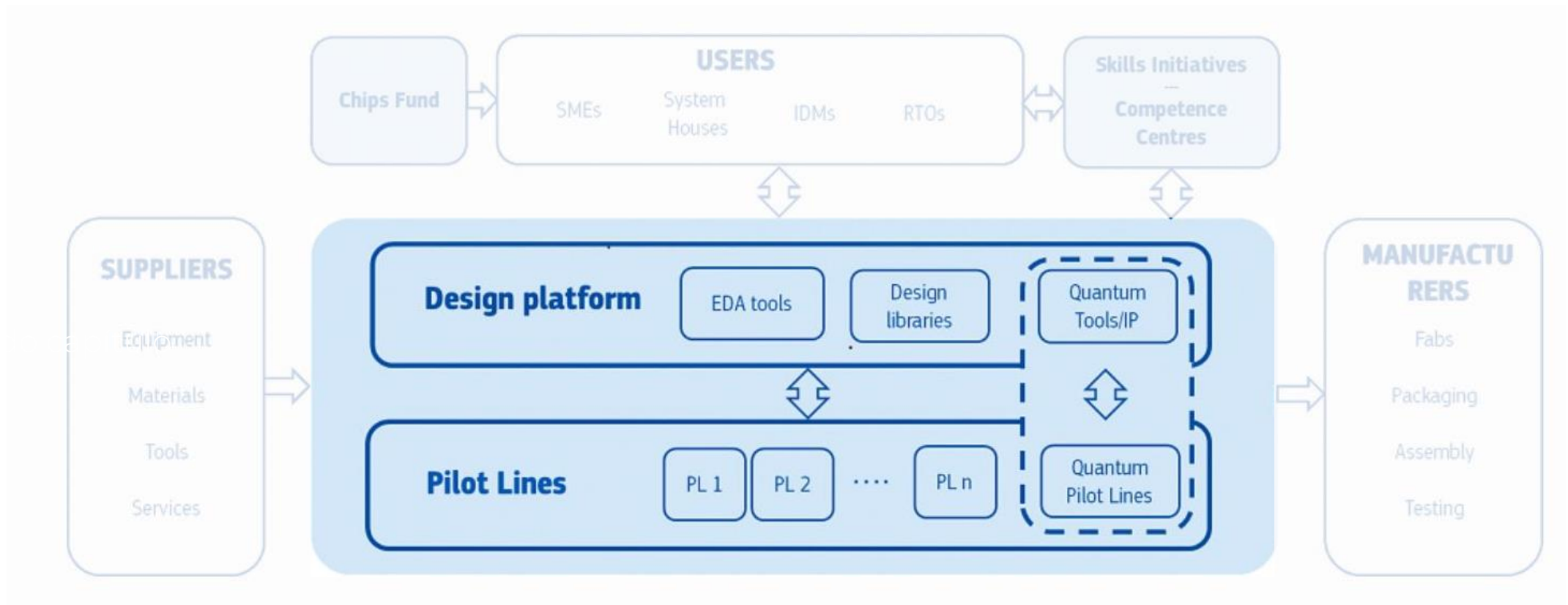
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- Network of national semiconductor competence Centers,
 - Design Platform,
 - Pilot Lines,
 - Quantum,
 - Chips Fund

EU Chips Act Pillar 1



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EU Chips Act Pillar 1



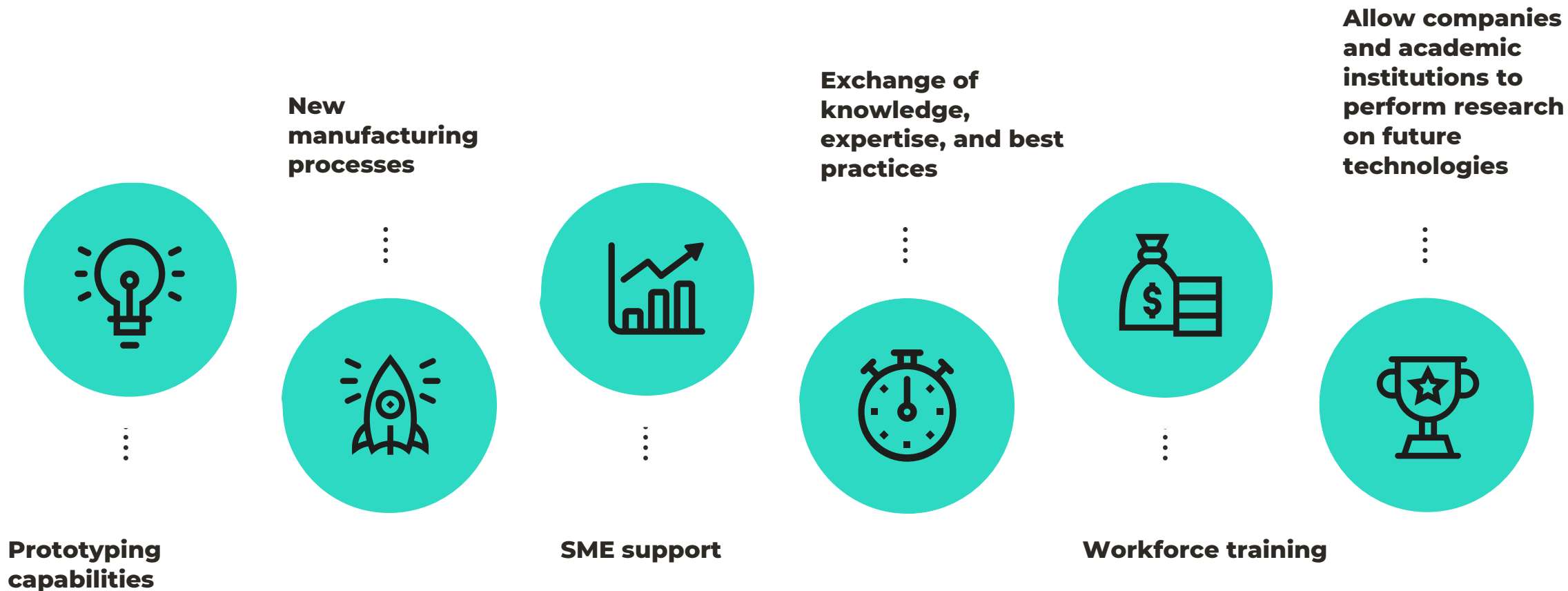


Pilot Lines

Supporting and accelerating **technological capacity building and innovation** in the Union by bridging the gap between the Union's advanced research and innovation capabilities and their industrial exploitation.

Intended to allow companies and academic institutions to perform research on future technologies.

Pilot Lines



Pilot Lines Calls

Opening date	Deadline	Action	Title	Maximum EU Expenditure (M€)
01-02-24	29-02-24	Chips 2023-CPL-1	Advanced sub 2nm leading-edge system on chip technology	700
		Chips 2023-CPL-2	Advanced Fully Depleted Silicon On Insulator technologies targeting 7nm	420
		Chips 2023-CPL-3	Advanced Packaging and Heterogenous Integration	370
		Chips 2023-CPL-4	Advanced semiconductor devices based on Wide Bandgap materials	180
08-07-24	17-09-24	Chips-2024-CPL-5	[...] for example a photonics pilot line.	Max. EURO 180 Mio
Total				1,850.00

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Design Platform

Develop a cloud-based virtual environment available across the Union, integrating a wide range of design facilities, from IP libraries to Electronic Design Automation tools, as well as support services.

With the potential to offer a valuable research, development and testing ground, a learning environment to advance innovation, enhance manufacturing capabilities, and accelerate the development of cutting-edge electronic products

Design Platform Call

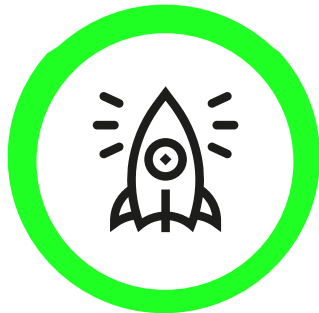


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**Support research in
cross-sectional
technologies**

**Validation of edge
AI and embedded
computing chips**

⋮

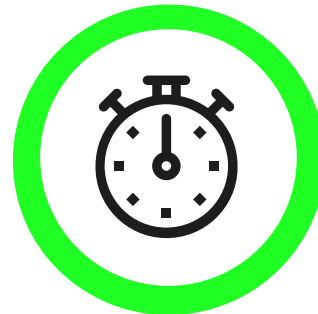


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**One-shop entry for
start-up/SMEs and
academia**

Support to IPs

⋮



⋮

**Support Open-
Source Hardware
community in
Europe (e.g. RISC V)**

**Provide access to
tools for embedded
AI used in EDA
challenges**

⋮



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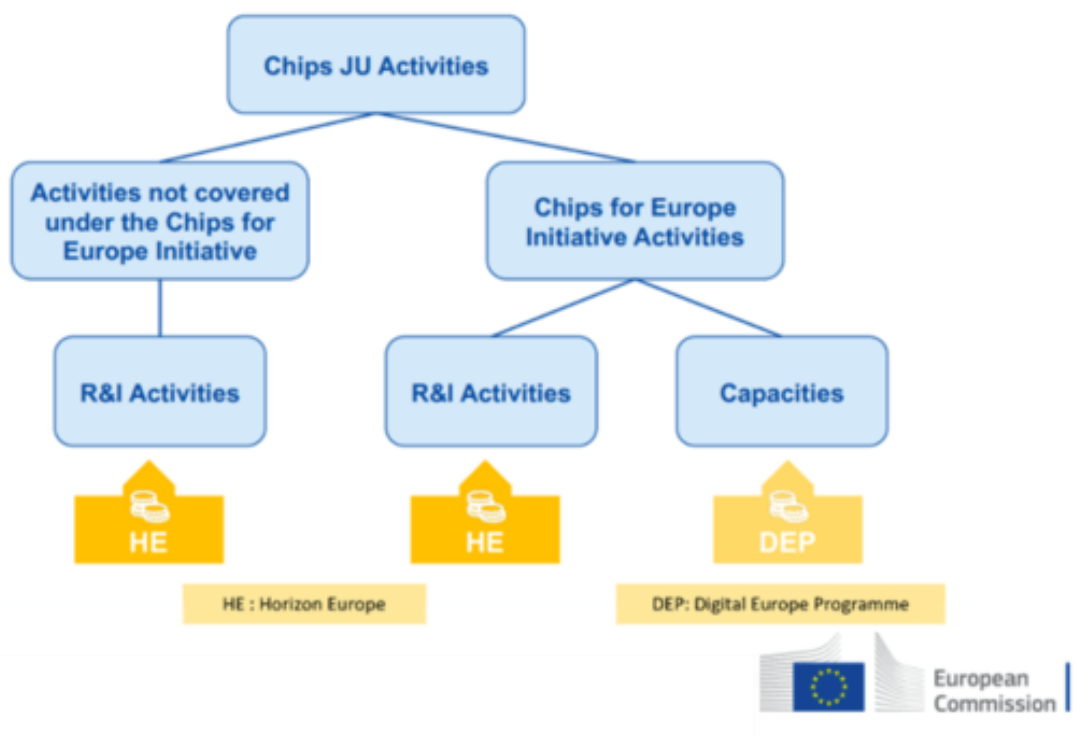
Design Platform Call

Opening date	Deadline	Action	Title	Maximum EU Expenditure (M€)
08-07-24	17-09-24	Chips-2024-CDP-1	Design platform	330
Total				330.00

*The Design Platform as foreseen in Pillar 1 of the European Chips Act, also referred to as the Chips for Europe Initiative, **shall consist of a cloud-based virtual platform that will enable users, particularly academia, start-ups and SMEs, to design and develop their chips.** The platform shall support users along the innovation process, in preparation for industrial deployment.*



CHIPS JU Activities



National co-funding in line with the National Strategy for Semiconductors (RCM 12/2024)

- Pilot Lines
- Design Platform
- [...]



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