Chips JU – National co-funding

Infoday Chips JU calls (2024) - 4 March, INL (Braga)

Mário Amaral (mario.amaral@fct.pt)



Infoday Chips JU calls (2024) - 4 March, INL (Braga)

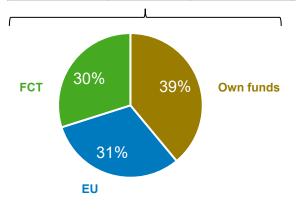
ECSEL and KDT:

Portuguese participation in R&I calls

Infoday Chips JU calls (2024) – 4 March, INL (Braga)

Portuguese participation in ECSEL and KDT (2014 – 2022)

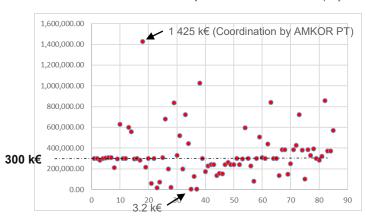
Type of entity	Total costs	EU contribution	FCT contribution	Number of PT participations
Company	17.471 M€	4.959 M€	2.940 M€	43
RTO	11.633 M€	4.102 M€	5.762 M€	42
Total	29.104 M€	9.060 M€	8.703 M€	85



> 8.7M€ of FCT co-funding leveraged 29.1M€ of total costs (2014 to 2022)



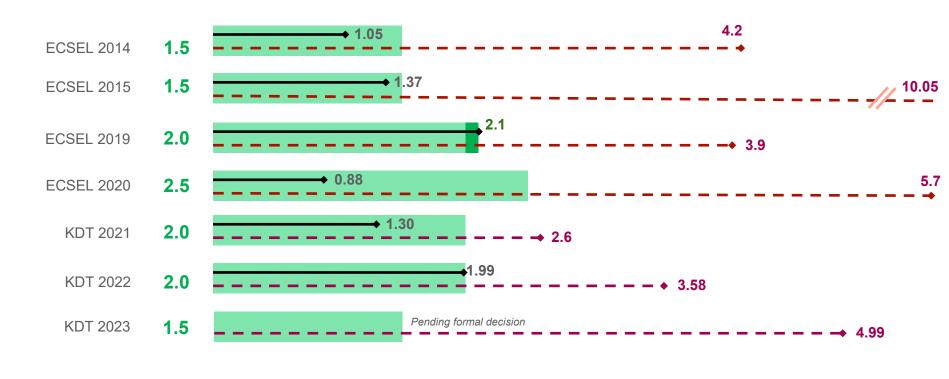
Distribution of total costs for PT partners in ECSEL e KDT projects

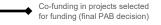


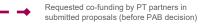
Infoday Chips JU calls (2024) - 4 March, INL (Braga)

National co-funding (FCT) in ECSEL and KDT R&I calls

fct







Infoday Chips JU calls (2024) - 4 March, INL (Braga)

<u>fct</u>

Portuguese participation in ECSEL and KDT projects

ECSEL 2014 ECSEL 2015

MANTIS	Cyber Physical System based Proactive Collaborative Maintenance
SWARMs	Smart and Networking UnderWAter Robots in Cooperation Meshes
SafeCOP	Safe Cooperating Cyber-Physical Systems using Wireless Communication
REFERENCE	Rf Engineered substrates to FostER fEm performaNCE
SemI40	Power Semiconductor and Electronics Manufacturing 4.0
ENABLE-S3	European Initiative to Enable Validation for Highly Automated Safe and Secure Systems

ECSEL 2016 ECSEL 2017 -**ECSEL 2018**

_	
I-MECH	Intelligent Motion Control Platform for Smart Mechatronic Systems
EuroPAT-MASIP	European Packaging, Assembly and Test Pilot for Manufacturing of Advanced System-in-Package
Productive4.0	Electronics and ICT as enabler for digital industry and optimized supply chain management covering the entire product lifecycle
SCOTT	Secure COnnected Trustable Things
POSITION-II	A pilot line for the next generation of smart catheters and implants
OCEAN12	Opportunity to Carry European Autonomous driviNg further with FDSOI technology up to 12nm node
AFarCloud	Aggregate Farming in the Cloud
SECREDAS	Cyber Security for Cross Domain Reliable Dependable Automated Systems
Arrowhead Tools	Arrowhead Tools for Engineering of Digitalisation Solutions

Portuguese participation in ECSEL and KDT projects



ECSEL 2019 ECSEL 2020

<u>fct</u>

Moore4	Medical A	Accelerating Innovation in Microfabricated Medical Devices	
InSecT1	- In	ntelligent Secure Trustable Things	
ADACO	RSA A	Airborne data collection on resilient system architectures	
ANDAN	TE A	Ai for New Devices And Technologies at the Edge	
VALU3S	S	/erification and Validation of Automated Systems' Safety and Security	
DAIS	D	Distributed Artificial Intelligent Systems	
IMOCO	4.E In	ntelligent Motion Control under Industry4.E	

KDT 2021 KDT 2022

OPEVA	OPEVA - OPtimization of Electric Vehicle Autonomy	
A-IQ Ready	Artificial Intelligence using Quantum measured Information for realtime distributed systems at the edge	
BRIGHTER	Breakthrough in micro-bolometer imaging	
ARROWHEAD FPVN	Arrowhead Flexible Production Value Networks	
EECONE	European ECOsystem for greeN Electronics	
SUSTRONICS	Sustainable and green electronics for circular economy	
NERVE REPACK	Intelligent neural system for bidirectional connection with exoprostheses and exoskeletons	
R-PODID	Reliable Powerdown for Industrial Drives	

Chips JU calls (2024) – 4 March, INL (Braga)

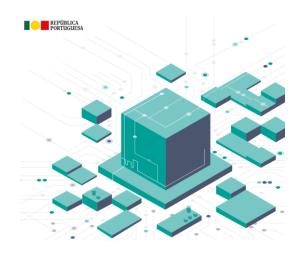
Chips JU: National co-funding

- R&I calls (non-initiative)
- Chips for Europe Initiative (Chips Act Pilar I):
 - Competence Centres

Infoday Chips JU calls (2024) – 4 March, INL (Braga)

National Strategy on Semiconductors

See full text here



Objectives:

- "Empowering Portugal in the microelectronics and semiconductor sector, ensuring a contribution to the EU's joint resilience and greater strategic autonomy, benefiting, and boosting the national scientific and technological system, for the creation of added value by our productive system"
- "Create a **Competence Centre** that aggregates the capacity distributed throughout the country [...]"
- "Involve industry in the strategic development process, mobilising the human and technological resources [...]"
- "Identify complementarities and synergies at national level [...] **boosting research**, **development**, **innovation and production** geared towards the technological needs of the global value chain"

Intervention axes:

- Axis 1: Strengthening specialised training and specific skills in microelectronics and semiconductors
- Axis 2: Expansion of the national chip design and advanced packaging ecosystem
- Axis 3: Increasing technological transfer in emerging areas

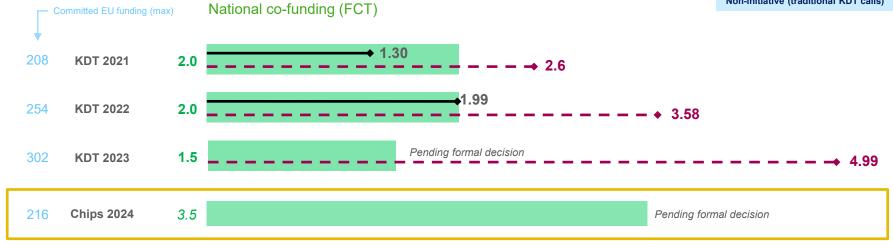
Budget (2024-27)

	Montante (Em euros) / Amount (In euros)			
	PT			EU / EU
Ano / Year	FCT - Fundação para a Ciência e a Tecnologia, I. P.	ANI - Agência Nacional de Inovação, S. A.	Montante total / Total amount	
2024	5 342 000	33 931 000	39 273 000	69 515 000
2025	5 225 000	30 977 000	36 202 000	63 463 000
2026	4 718 000	18 139 000	22 857 000	37 162 000
2027	4 715 000	18 069 000	22 784 000	37 018 000
Total	15 282 000	101 116 000	121 116 000	207 158 000

R&I calls (non-initiative)



Non-initiative (traditional KDT calls)



Infoday Chips JU calls (2024) – 4 March, INL (Braga)

Competence Centre for Semiconductors ("Chips CC")

Axis 1 of the National Strategy on Semiconductors:

Reinforcement of specialised training and specific skills in the area of microelectronics and semiconductors: creation in Portugal of a Competence Centre dedicated to the areas of microelectronics and semiconductors, enabling it as a national entity responsible for:



Chips for Europe Initiative

- Specialised training and support for the national ecosystem
- Access to the international network and the EU Chips Act tools

National pre-selection (*draft information – tbc*):

- Candidate consortia must have broad national coverage (minimum 3 entities)
- > Evaluation criteria:
 - Relevance; Implementation; Impact (== Chips JU call)
 - Technological Scope: chip design, adv. packaging, emerging areas (photonics, cointegration, flexible and sustainable electronics and sensors)
- > Pre-selection of **one** national consortium

Chips JU selection (WP 2024):

Pre-selected national candidate may apply to the Chips JU call

Funding: 50% EU + 50% National for 4 years

- ➤ National co-funding (FCT) up to 1M€
- ➤ EU co-funding (Chips JU) up to 1M€

The national co-funding will be indicative and conditioned to the final selection of the pre-selected candidate in the Chips JU selection

WP 2024	National pre-selection	Chips JU selection
1 st wave of Chips CCs	By end of May 2024 (tbc)	08/07/2024 – 17/09/2024

Thank you for your attention

